

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) Tuning circuit for tuning a filter stage, wherein the filter stage comprises: ~~which has an RC element with an RC time constant, with the RC time constant being the product of a resistor in the RC element and the capacitance of a capacitor, which is connected in series with the resistor, in the RC element, having:~~
 - (a) at least one RC element with an RC time constant, with the RC time constant being the product of the resistance of a resistor and the capacitance of a capacitor which is connected at a potential node to the resistor; and ~~a comparator for comparison of the voltage which is produced at the potential node between the resistor and the capacitor, with a reference ground voltage; and having~~
 - (b) an operational amplifier including a first signal input which is connected to the potential node and including a signal output which is fed back via the capacitor to the first signal input of the operational amplifier; a controller ~~which varies the charge on the capacitor in the RC element until the comparator indicates that the voltage which is produced at the potential node is equal to the reference ground voltage, with the controller switching a capacitor array as a function of the charge variation time, which capacitor array is connected in parallel with the capacitor in the RC~~

~~element, in order to compensate for any discrepancy between the RC time constant of the RC element and a nominal value.~~

wherein the tuning circuit comprises:

(c) a comparator including a first input which is connected to the signal output of the operational amplifier and including a second input which is alternatively connectable to a first reference voltage source which generates a first reference voltage, or to a second reference voltage source which generates a second reference voltage; and

(d) a controller which is connected to the comparator, the controller including a sequence controller,

wherein the capacitor has its charge varied by means of switches which are controlled by the sequence controller, until the first reference voltage is applied to the first input of the comparator, and the capacitor then has its charge varied in the opposite direction by means of switches which are controlled by the sequence controller until the second reference voltage is applied to the first input of the comparator,

wherein a digital counter contained in the sequence controller records a charge variation time for varying the charge of the capacitor;

(e) wherein the controller switches a capacitor array as a function of the recorded charge variation time, and

wherein the capacitor array is connected in parallel with the capacitor of the RC element in order to compensate for any discrepancy between the

RC time constant of the RC element and a nominal value of the RC time constant.

2. (Previously Presented) Tuning circuit according to Claim 1, wherein the filter stage is contained in an integrated analog filter.
3. (Canceled)
4. (Canceled)
5. (Currently Amended) Tuning circuit according to Claim [[4]] 1, wherein the switches are CMOS switches.
6. (Canceled)
7. (Currently Amended) Tuning circuit according to Claim [[6]] 1, wherein the digital counter [[for]] of the sequence controller is clocked by means of an external clock signal.
8. (Currently Amended) Tuning circuit according to Claim [[7]] 1, wherein the digital counter for the sequence controller counts [[the]] a number of clock cycles from the external clock signal between reception of a start signal and reception of a stop signal which is received from the comparator.
9. (Previously Presented) Tuning circuit according to Claim 1, wherein the controller has a memory which is connected to the sequence controller.
10. (Currently Amended) Tuning circuit according to Claim [[9]] 9, wherein a coded tuning control signal for switching the capacitor array is stored in the memory for each count of the digital counter.

11. (Previously Presented) Tuning circuit according to Claim 10, wherein the capacitor array has two or more tuning capacitors which are connected in parallel with the capacitor in the RC element as a function of the coded tuning control signal.
12. (Canceled)
13. (Previously Presented) Tuning circuit according to Claim 1, wherein the capacitor array is integrated in the filter stage.
14. (Currently Amended) Tuning circuit according to Claim ~~[[3]]~~ 2, wherein the integrated analog filter ~~can be switched~~ is switchable between a normal filter mode and a tuning mode by means of switches which are controlled by the sequence controller.
15. (Canceled)
16. (Canceled)
17. (Canceled)
18. (Canceled)
19. (Canceled)
20. (Canceled)
21. (Canceled)
22. (Canceled)
23. (Canceled)
24. (Canceled)
25. (Canceled)

26. (Previously Presented) Tuning circuit according to Claim 2, wherein the analog filter is an anti-aliasing filter.
27. (Previously Presented) Tuning circuit according to Claim 26, wherein the analog filter is an xDSL anti-aliasing filter.
28. (Previously Presented) Tuning circuit according to Claim 2, wherein the analog filter is a biquad filter.
29. (Currently Amended) Tuning circuit according to Claim 2, wherein the analog filter is ~~completely~~ a differential analog filter.
30. (Currently Amended) Tuning circuit according to Claim ~~[[3]]~~ 2, wherein the analog filter has two or more filter stages.
31. (Currently Amended) Method for tuning a filter stage, the filter stage includes ~~which contains~~ an RC element with an RC time constant, ~~[[with]]~~ wherein the RC time constant ~~[[being]]~~ is the product of the resistance of a resistor in the RC element and the capacitance of a capacitor in the RC element, ~~which the capacitor is connected in series with the resistor, in of the RC element, [[with]] the method comprising having the following steps:~~
 - (a) ~~variation of~~ varying the charge on the capacitor by at least one RC element in the filter stage;
 - (b) ~~measurement of~~ measuring the charge variation time until the voltage which is produced at the potential node between the resistor and the capacitor in the RC element reaches a reference ~~ground~~ voltage;

- (c) switching a capacitor array, which is connected in parallel with the capacitor in the RC element, as a function of the measured charge variation time, in order to compensate for any discrepancy between the RC time constant of the RC element and a predetermined nominal value of the RC time constant; and
- (d) charging the capacitor in the element to a specific first reference voltage before the tuning of the filter stage;
- (e) wherein, in order to tune the filter stage, varying the charge on the capacitor until the voltage which is dropped across the capacitor is equal to a second reference voltage, and it is then charged in the opposite direction until the voltage which is dropped across the capacitor is once again equal to the first reference voltage.

- 32. (Canceled)
- 33. (Canceled)
- 34. (Canceled)
- 35. (Canceled)
- 36. (Canceled)